**DAILY ASSESSMENT REPORT**

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| **Date:** | **11 June 2020** | **Name:** | **Rubesh Murthy G** |
| **Course:** | **PCB Designing** | **USN:** | **4AL18EC044** |
| **Topic:** | **Making a schematic** | **Semester & Section:** | **4th sem ‘A’ sec** |
| **GitHub Repository:** | **Rubesh-Pythonite** |  |  |

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| **FORENOON SESSION DETAILS** |
| **Image of session** |
| **Report**  **Session was all about:**   * **Building the circuit diagram.** * **Making the schematic.** * **Label of signals.** * **Annotation.** |

**DAILY ASSESSMENT REPORT**

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| **Date:** | **11 June 2020** | **Name:** | **Rubesh Murthy G** |
| **Course:** | **Java** | **USN:** | **4AL18EC044** |
| **Topic:** | **First Code** | **Semester & Section:** | **4th sem ‘A’ sec** |
| **Github Repository:** | **Rubesh-Pythonite** |  |  |

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| **AFTERNOON SESSION DETAILS** |
| **Image of session** |
| **Report**  **Session was all about:**   * **Introduction to IntelliJ.** * **Introduction to Java Code.** * **Your First Java Code.** * **How to get the source code?** * **How java code builds up.** |